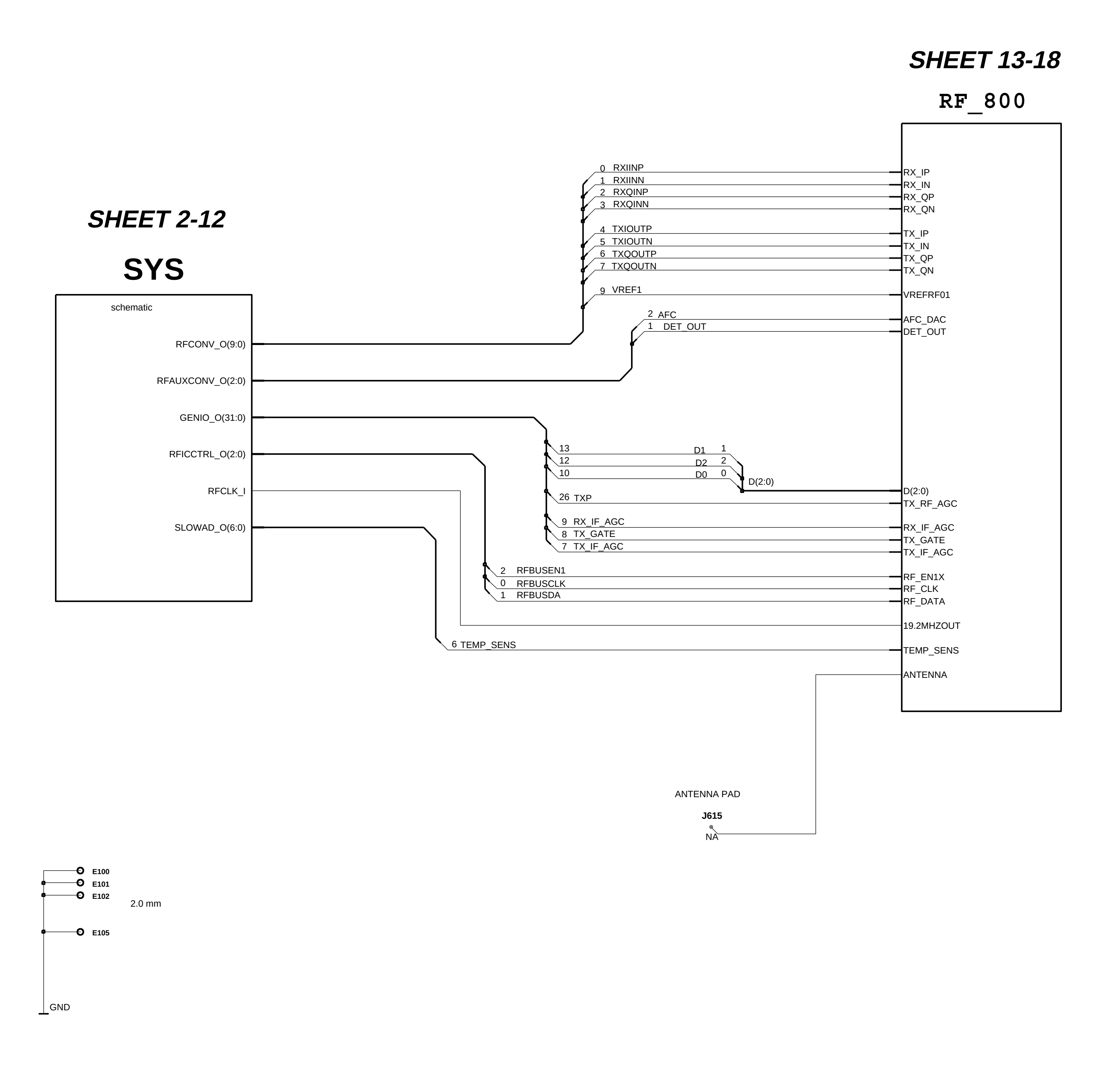
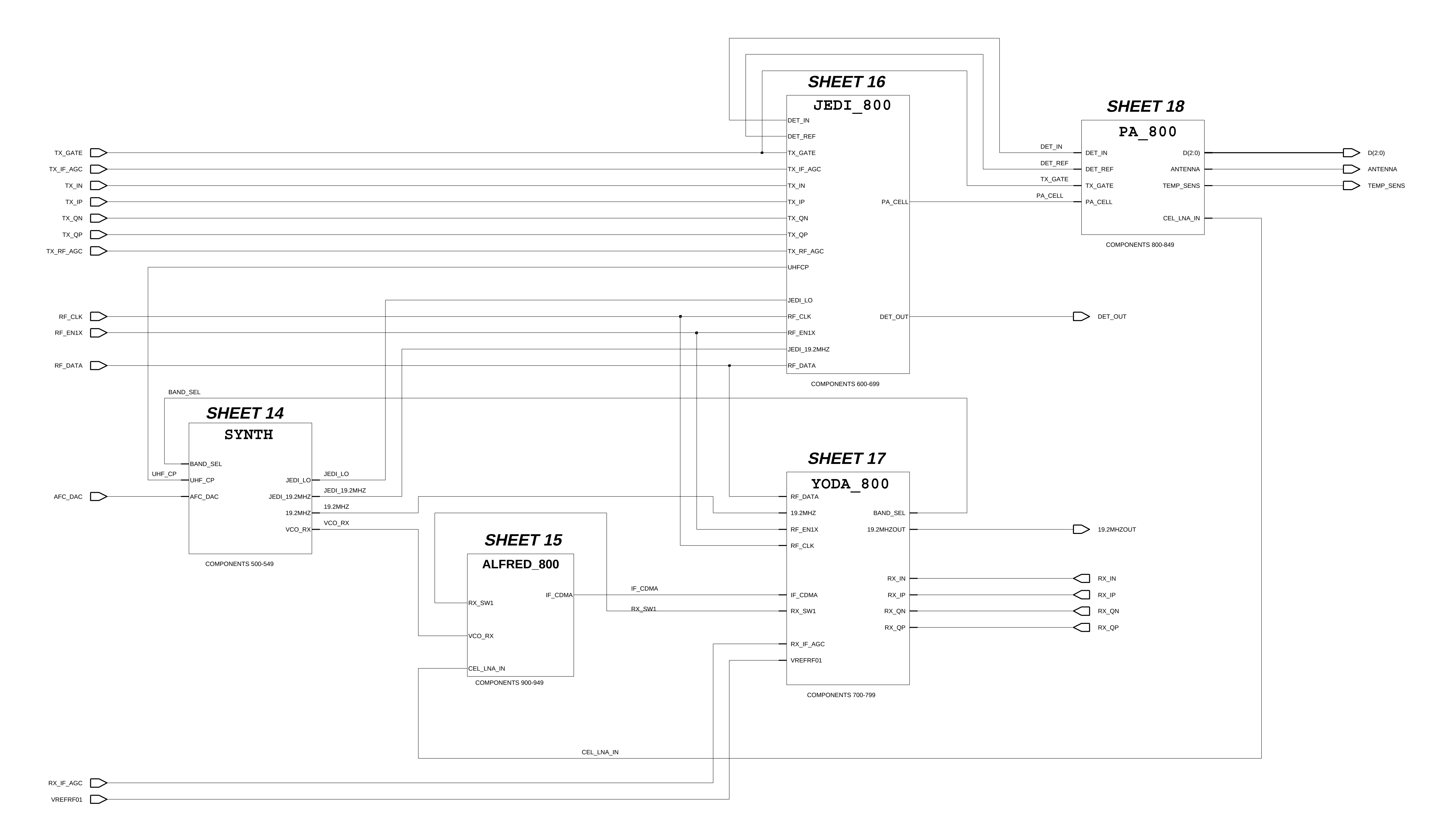
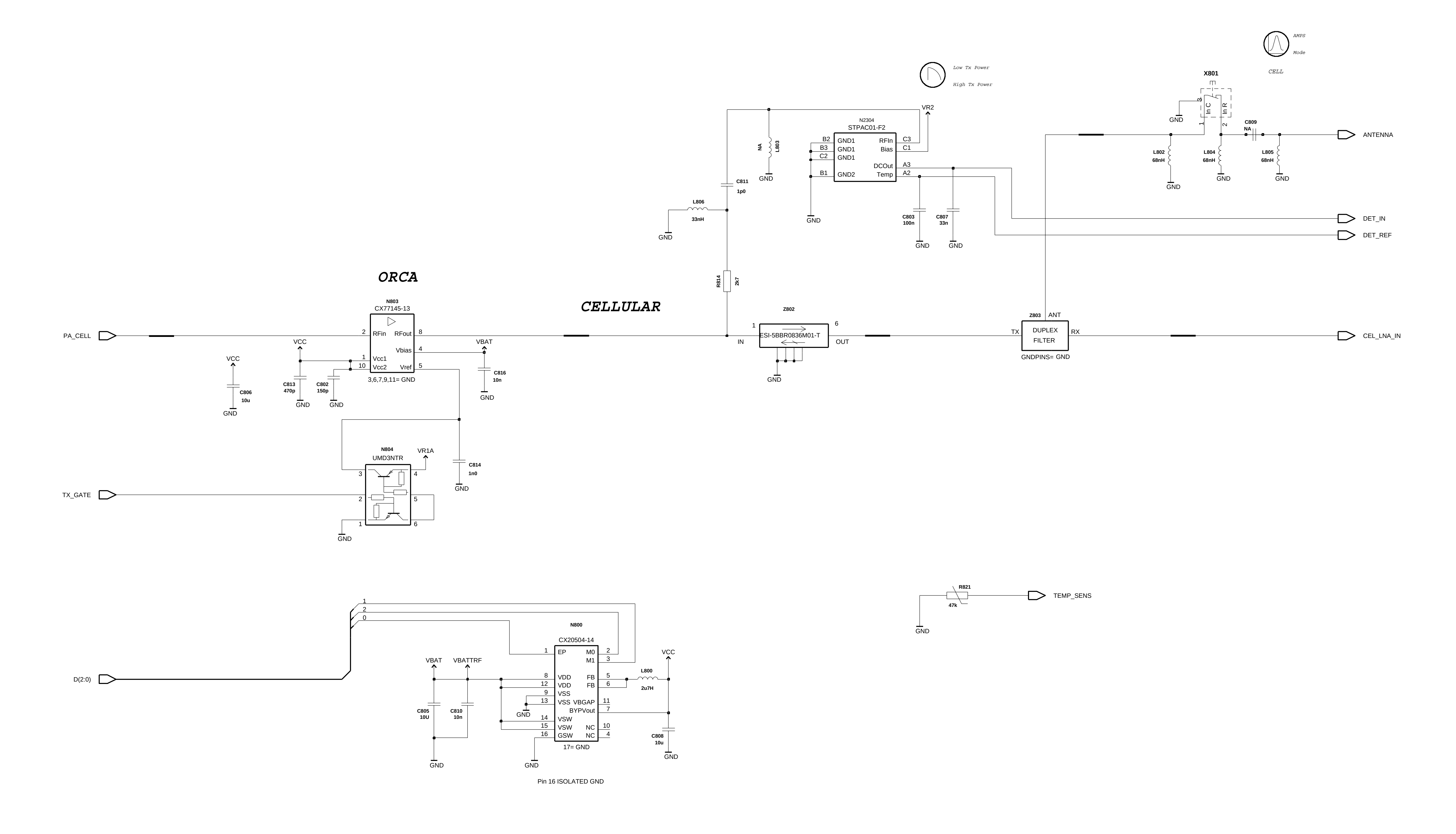
Top-Level Diagram



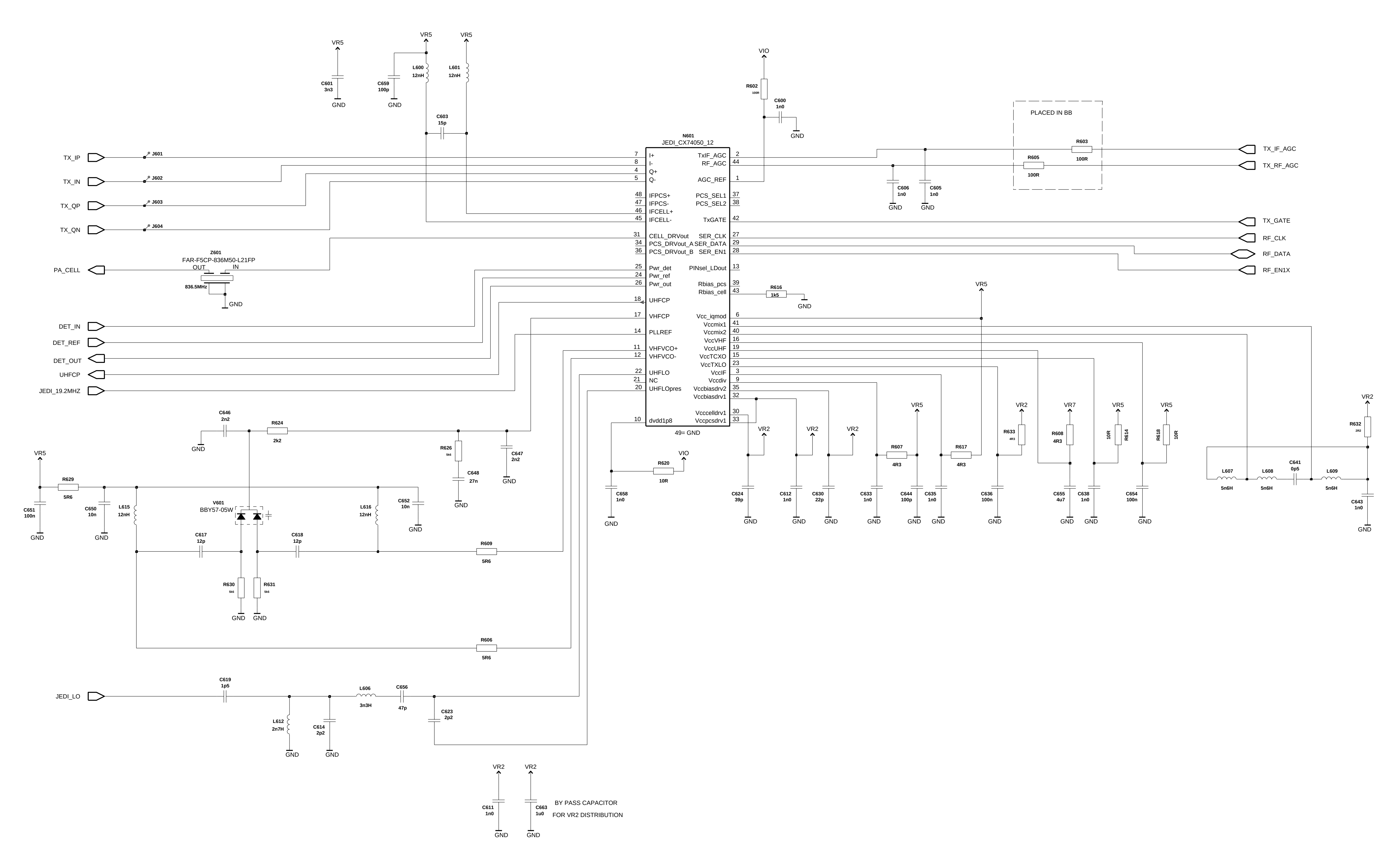
RF Top Level


SHEET 16
JEDI_800
DET_IN
DET_REF
TX_GATE
TX_IF_AGC
TX_IN
TX_IP
TX_QN
TX_QP
TX_RF_AGC
UHFCP
JEDI_LO
RF_CLK
RF_EN1X
JEDI_19.2MHZ
RF_DATA
PA_CELL
DET_OUT
COMPONENTS 600-699
SHEET 18
PA_800
DET_IN
DET_REF
TX_GATE
PA_CELL
D(2:0)
ANTENNA
TEMP_SENS
CEL_LNA_IN
COMPONENTS 800-849
SHEET 14
SYNTH
BAND_SEL
UHF_CP
AFC_DAC
JEDI_LO
JEDI_19.2MHZ
19.2MHZ
VCO_RX
COMPONENTS 500-549
SHEET 15
ALFRED_800
RX_SW1
VCO_RX
CEL_LNA_IN
IF_CDMA
COMPONENTS 900-949
SHEET 17
YODA_800
RF_DATA
19.2MHZ
RF_EN1X
RF_CLK
IF_CDMA
RX_SW1
RX_IF_AGC
VREFRF01
BAND_SEL
19.2MHZOUT
RX_IN
RX_IP
RX_QN
RX_QP
COMPONENTS 700-799
TX_GATE
TX_IF_AGC
TX_IN
TX_IP
TX_QN
TX_QP
TX_RF_AGC
RF_CLK
RF_EN1X
RF_DATA
BAND_SEL
UHF_CP
AFC_DAC
RX_IF_AGC
VREFRF01
DET_OUT
19.2MHZOUT
CEL_LNA_IN

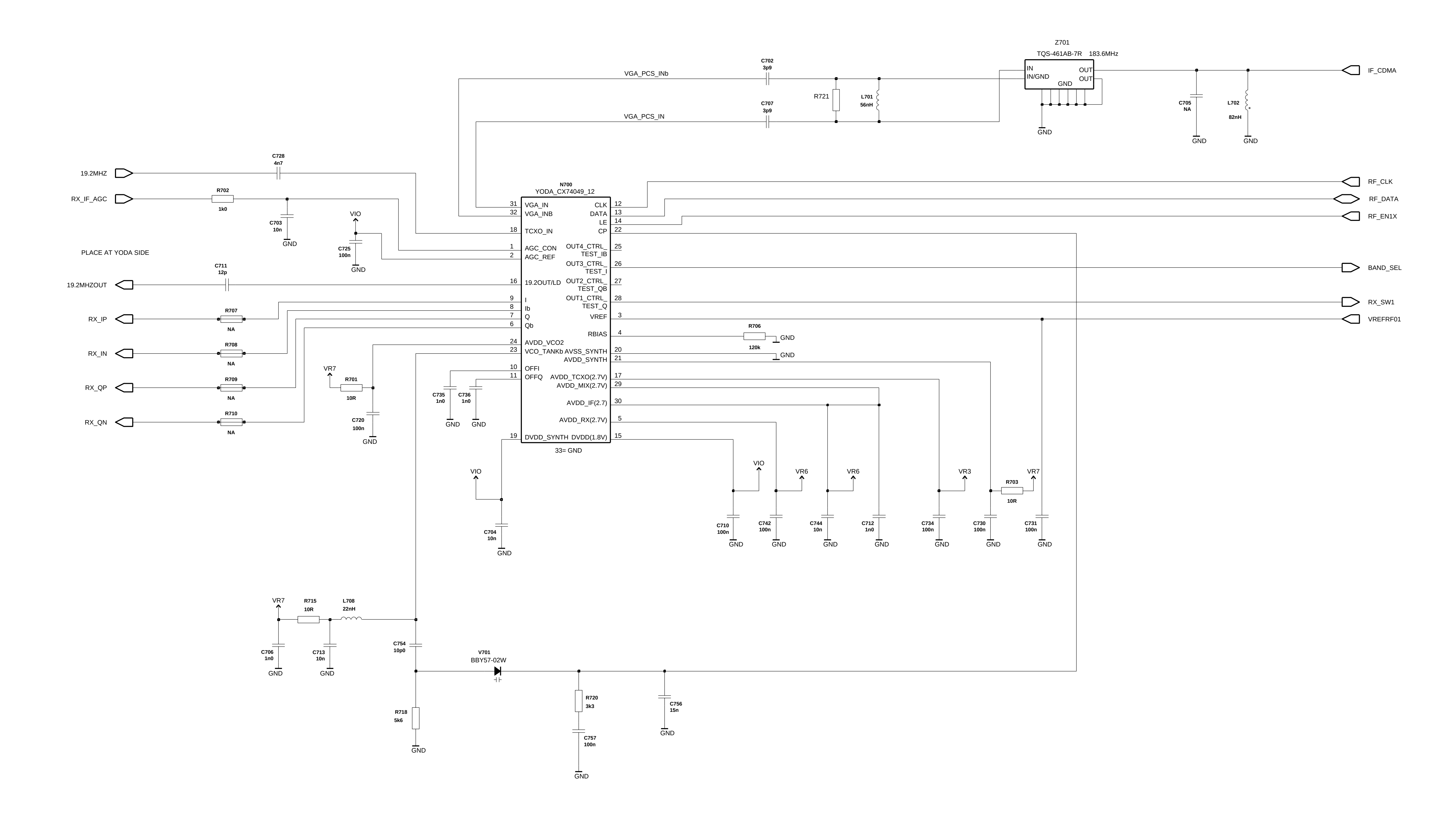
PA


ORCA
CELLULAR
N803
CX77145-13
N804
UMD3NTR
N2304
STPAC01-F2
Z802
ESI-5BBR0836M01-T
Z803
DUPLEX
FILTER
X801
N800
CX20504-14
PA_CELL
TX_GATE
D(2:0)
ANTENNA
DET_IN
DET_REF
CEL_LNA_IN
TEMP_SENS
Low Tx Power
High Tx Power
AMPS
Mode
CELL
Pin 16 ISOLATED GND

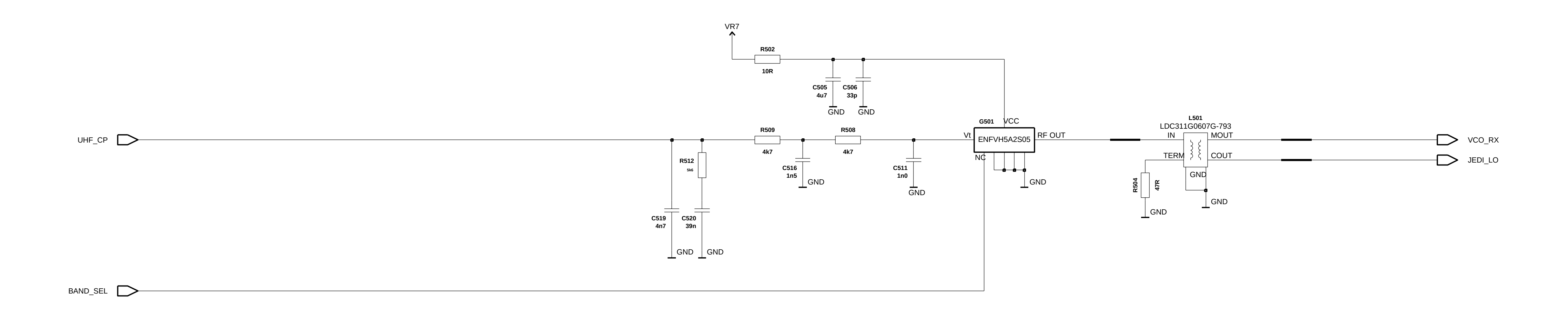
Jedi

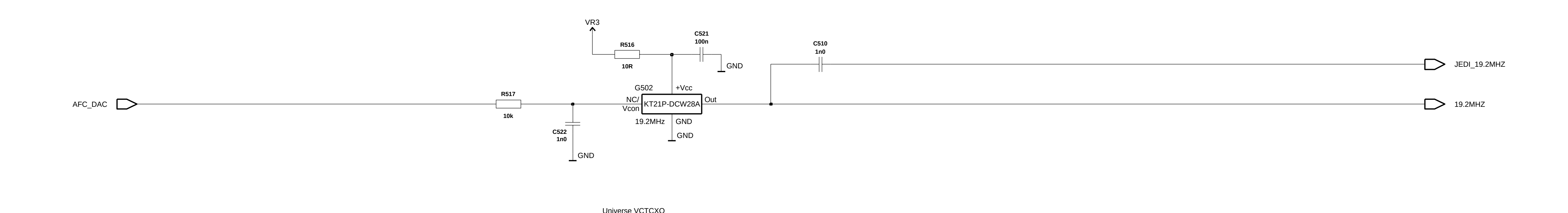


Yoda

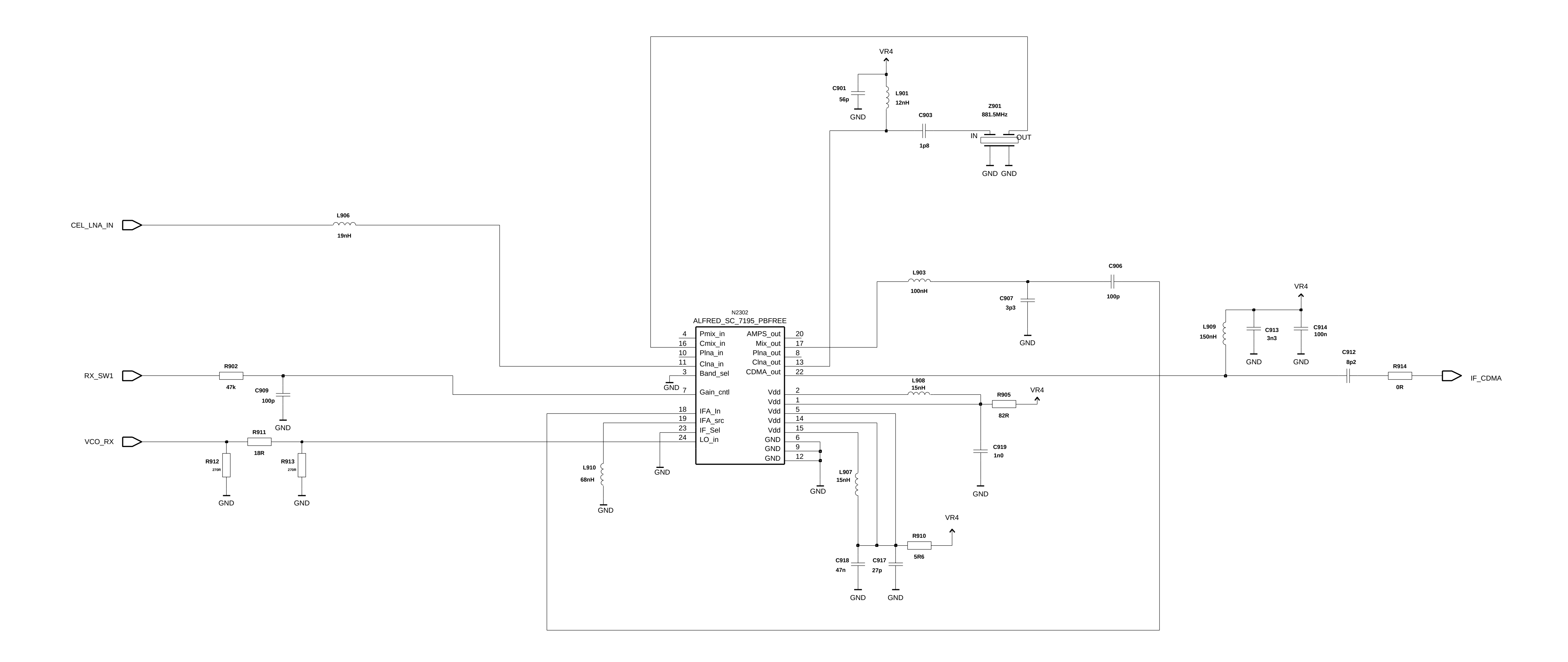


Synthesizer

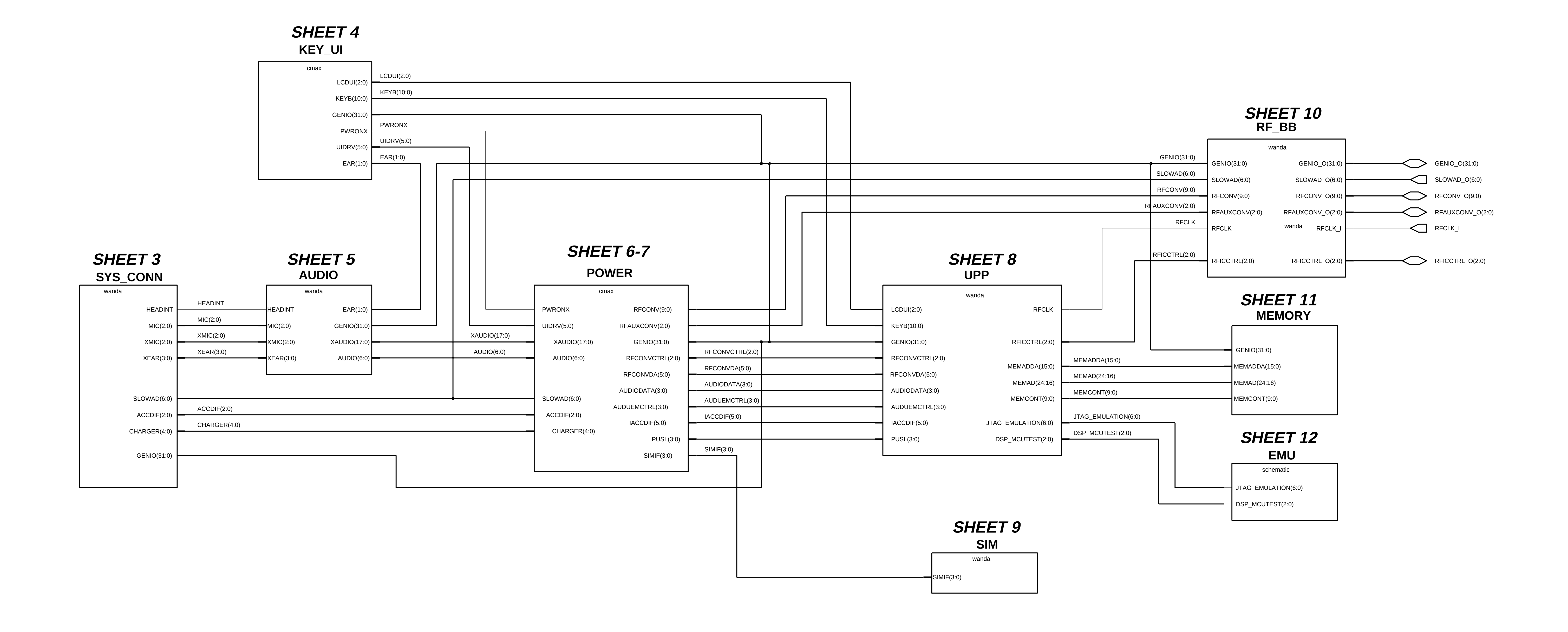




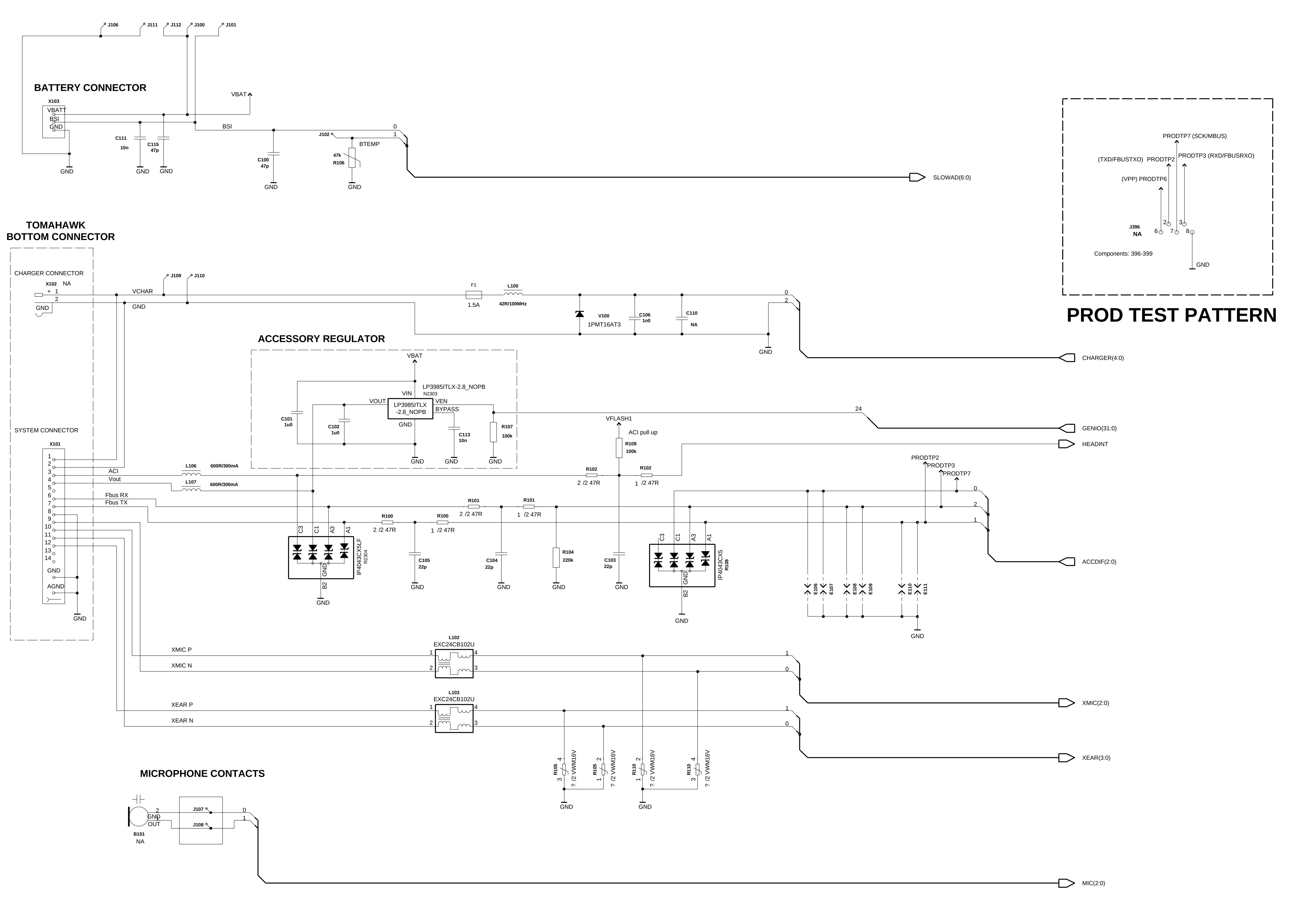
Alfred



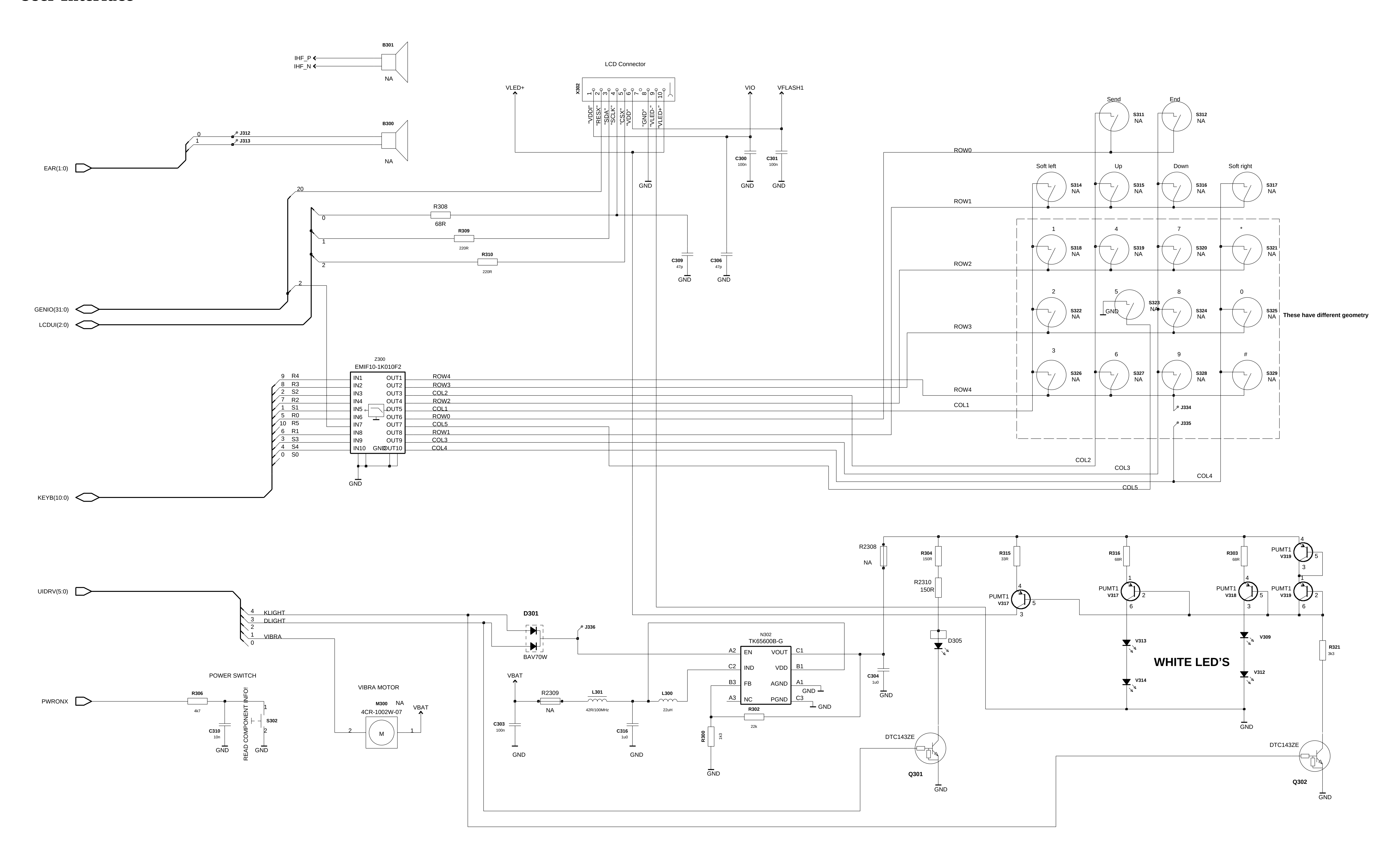
DCT4 Common Baseband



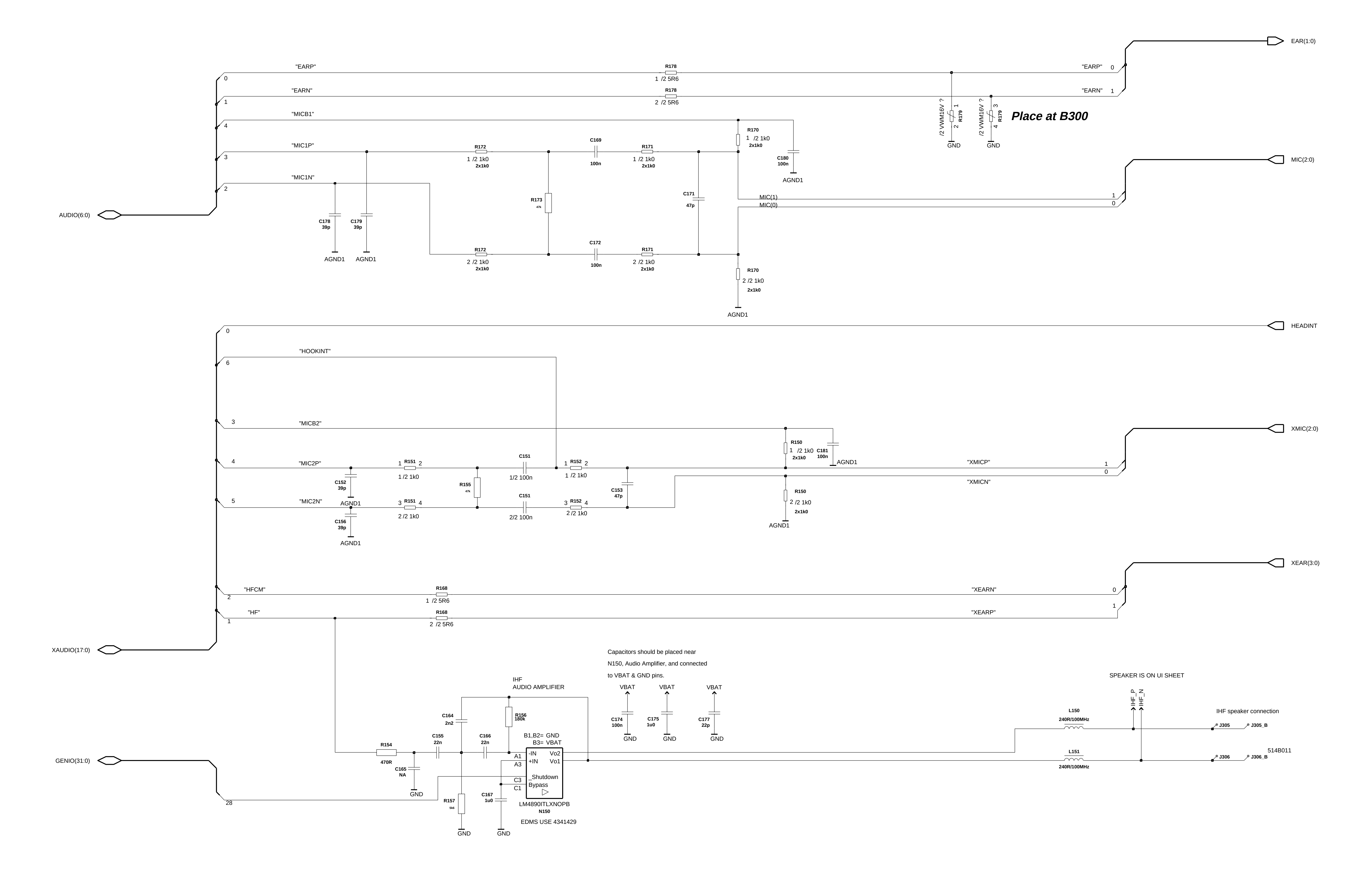
System Connector



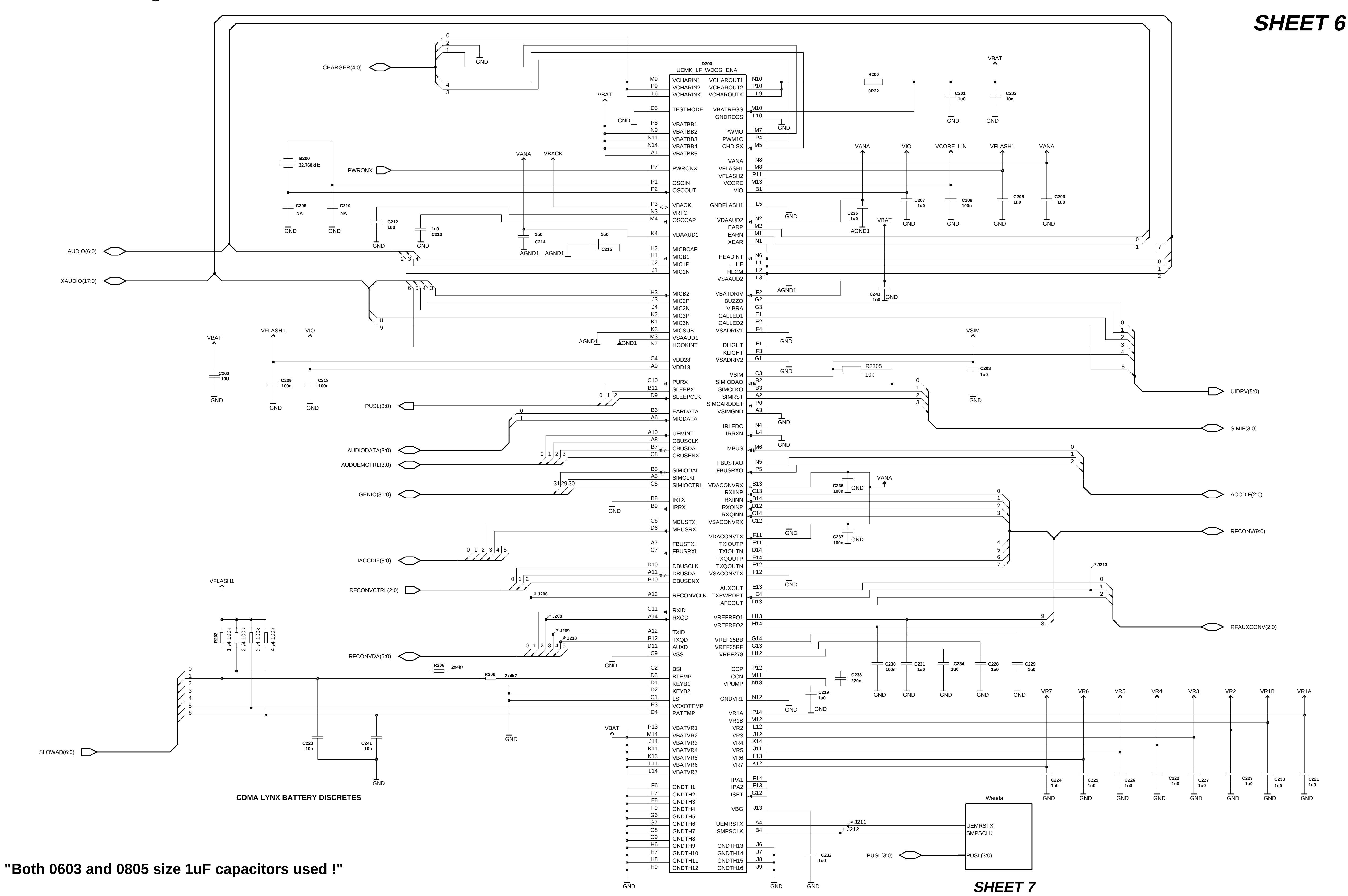
User Interface



Audio

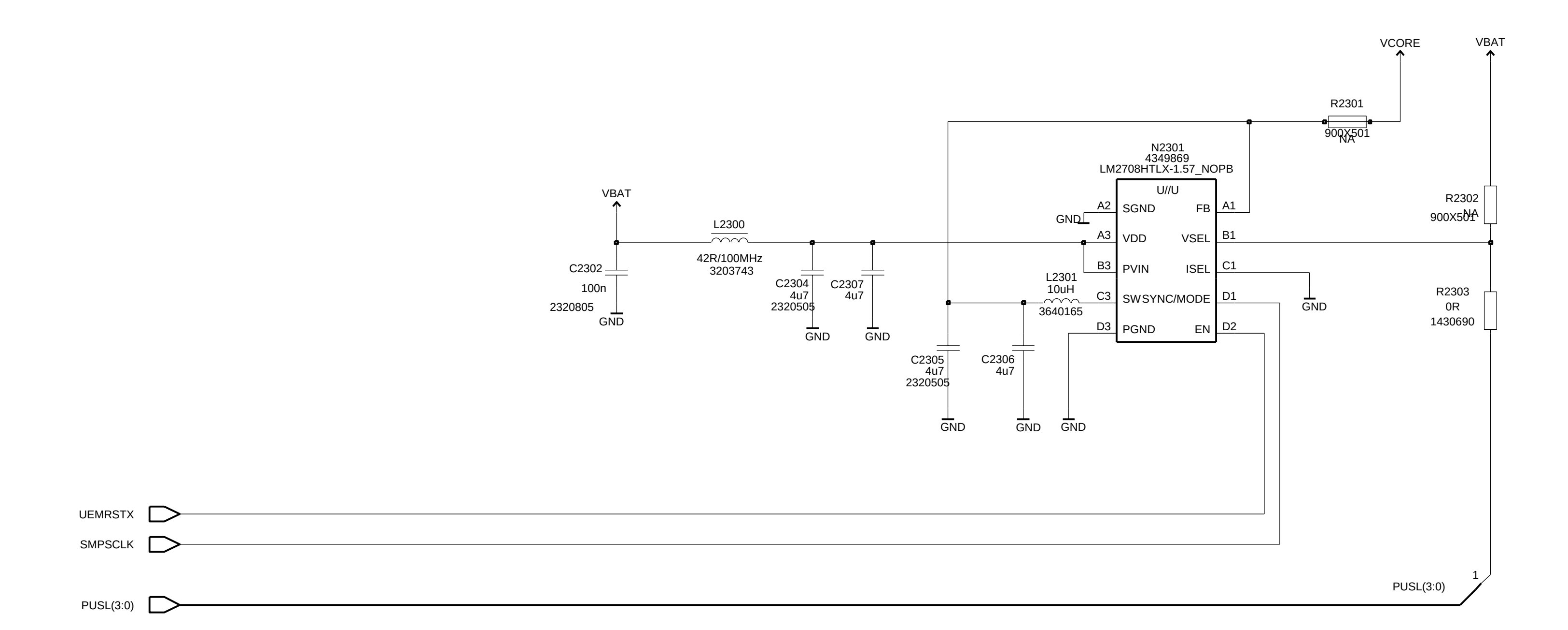

Place at B300
EAR(1:0)
MIC(2:0)
AUDIO(6:0)
HEADINT
XMIC(2:0)
XEAR(3:0)
XAUDIO(17:0)
GENIO(31:0)
"EARP"
"EARN"
"MIC1P"
"MIC1N"
"MICB1"
"HOOKINT"
"MICB2"
"MIC2P"
"MIC2N"
"XMICP"
"XMICN"
"HFCM"
"HF"
"XEARN"
"XEARP"
Capacitors should be placed near
N150, Audio Amplifier, and connected
to VBAT & GND pins.
IHF
AUDIO AMPLIFIER
SPEAKER IS ON UI SHEET
IHF speaker connection
LM4890ITLXNOPB
N150
EDMS USE 4341429
514B011

Discrete Power Management

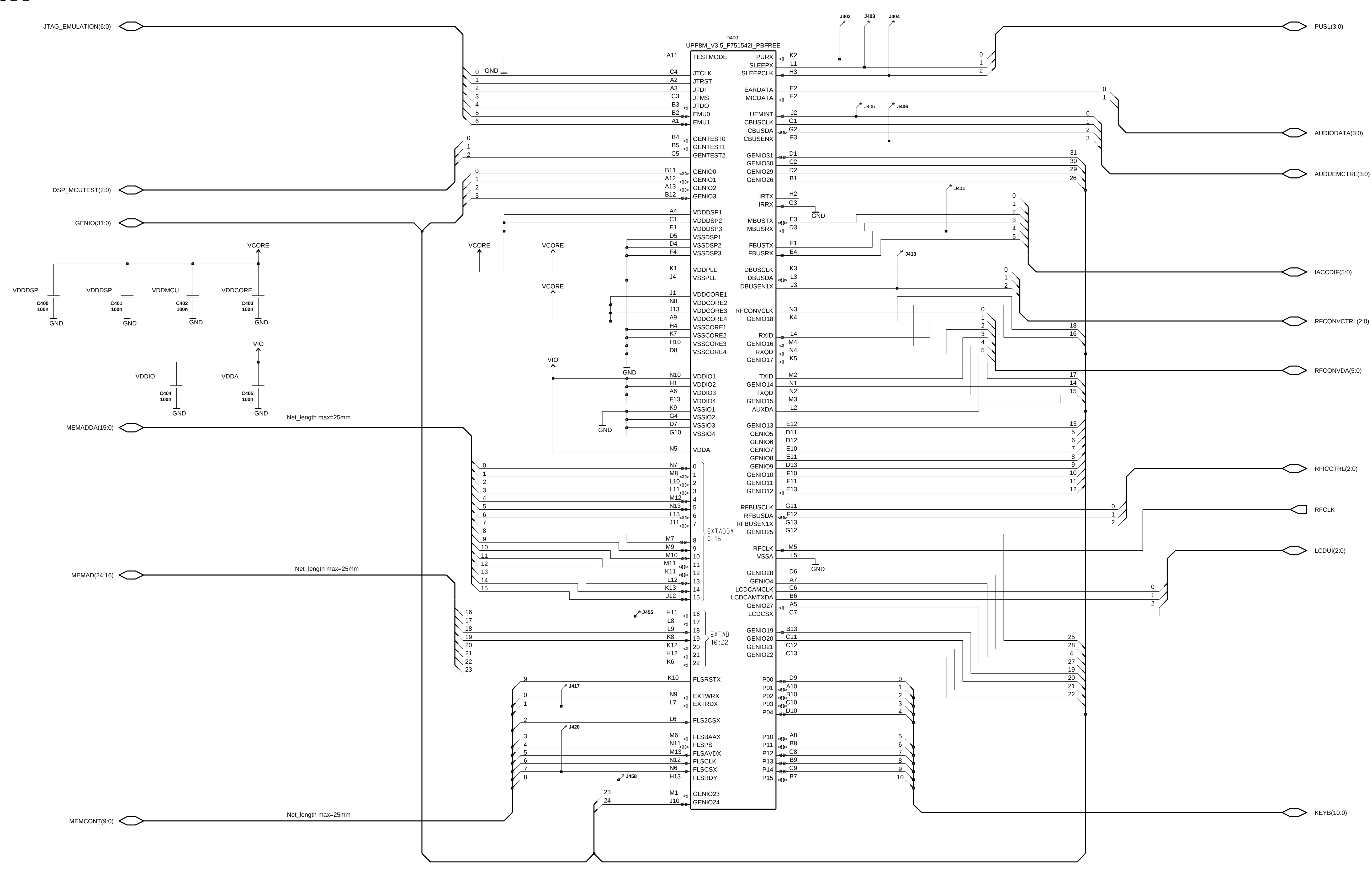


"Both 0603 and 0805 size 1uF capacitors used !"

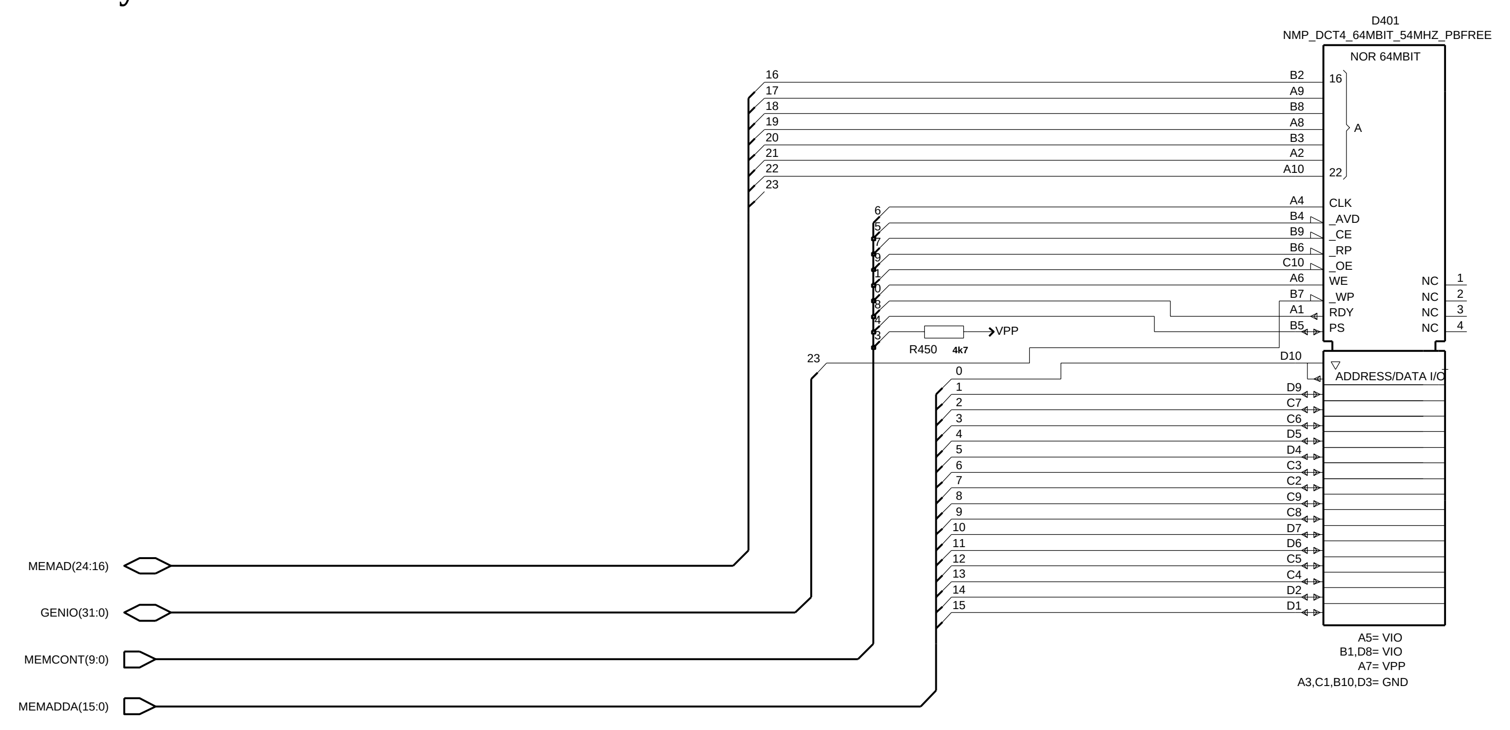
DC/DC Converter

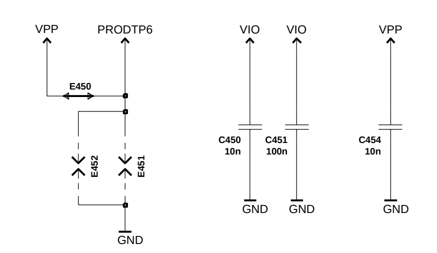


UPP

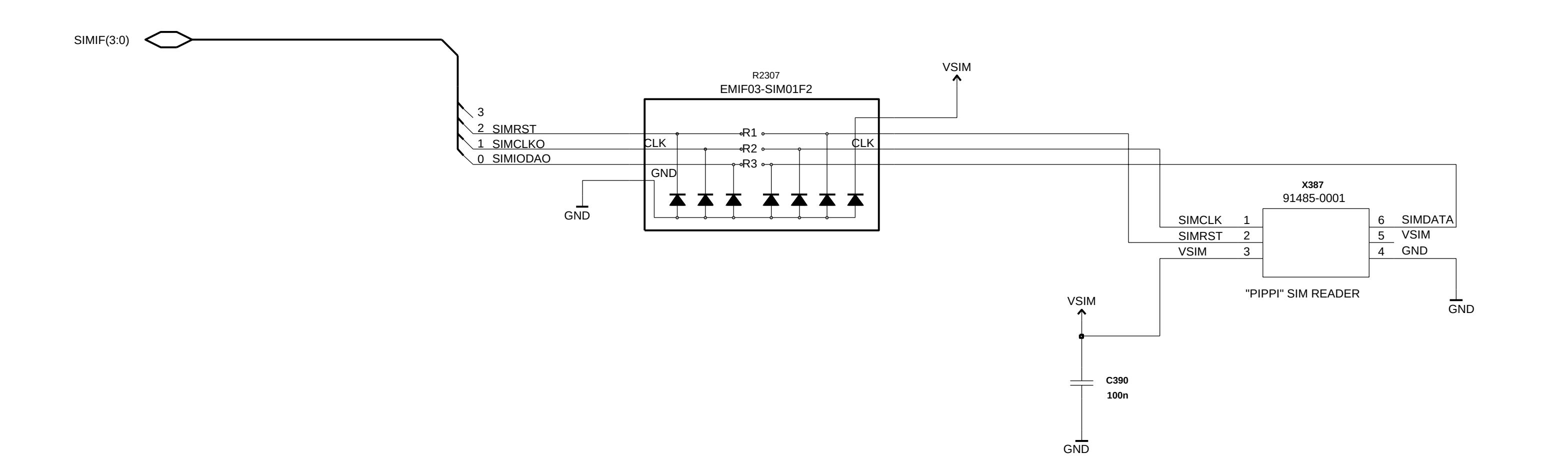

JTAG_EMULATION(6:0)
DSP_MCUTEST(2:0)
GENIO(31:0)
MEMADDA(15:0)
MEMAD(24:16)
MEMCONT(9:0)
D400
UPP8M_V3.5_F751542I_PBFREE
PUSL(3:0)
AUDIODATA(3:0)
AUDUEMCTRL(3:0)
IACCDIF(5:0)
RFCONVCTRL(2:0)
RFCONVDA(5:0)
RFICCTRL(2:0)
RFCLK
LCDUI(2:0)
KEYB(10:0)
VDDDSP
VDDMCU
VDDCORE
VCORE
VDDIO
VDDA
VIO
GND
C400 100n
C401 100n
C402 100n
C403 100n
C404 100n
C405 100n
Net_length max=25mm
EXTADDA 0:15
EXTAD 16:22
J402
J403
J404
J405
J406
J411
J413
J417
J420
J455
J458

Combo Memory 128+8 Mbit

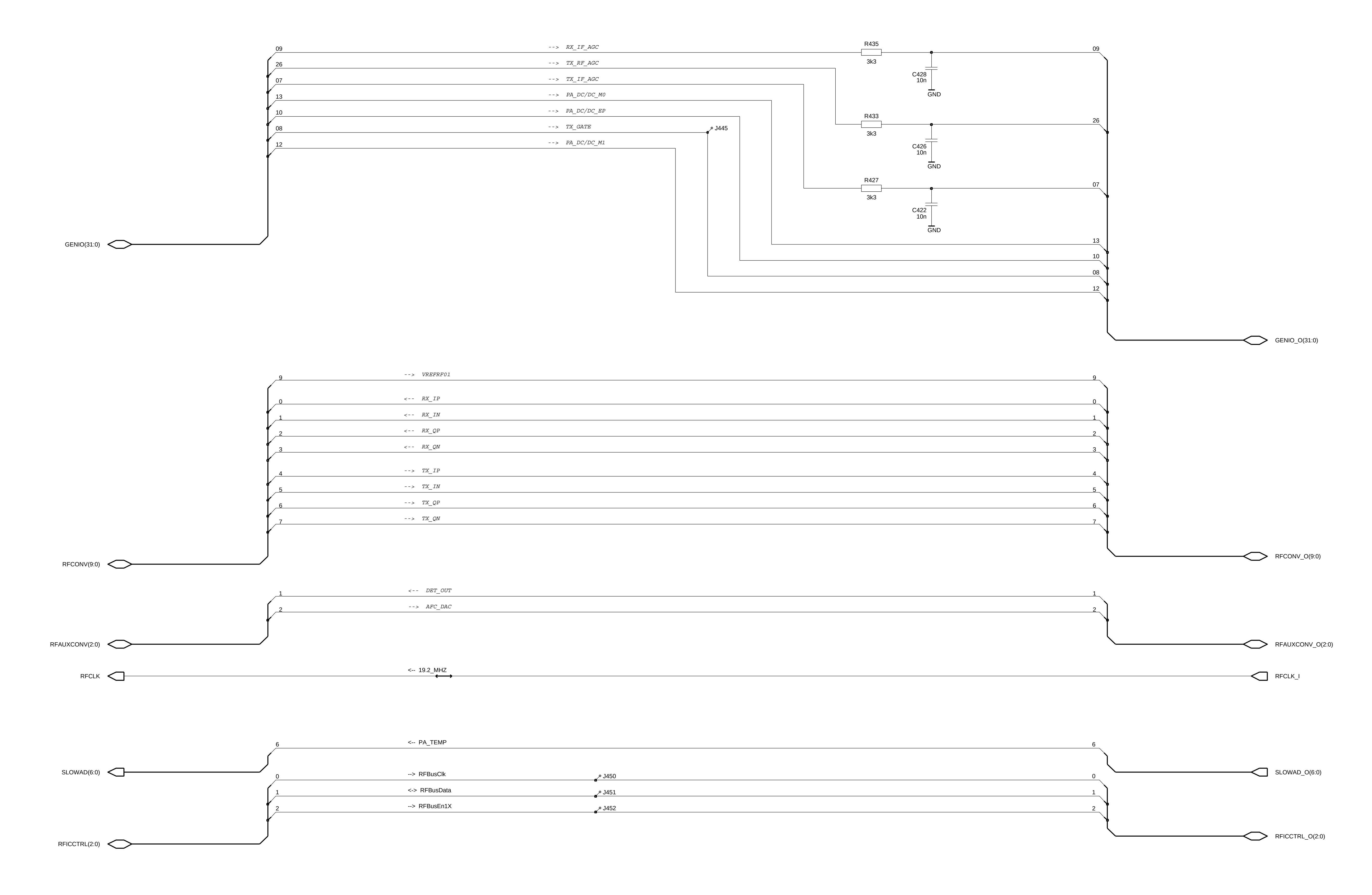




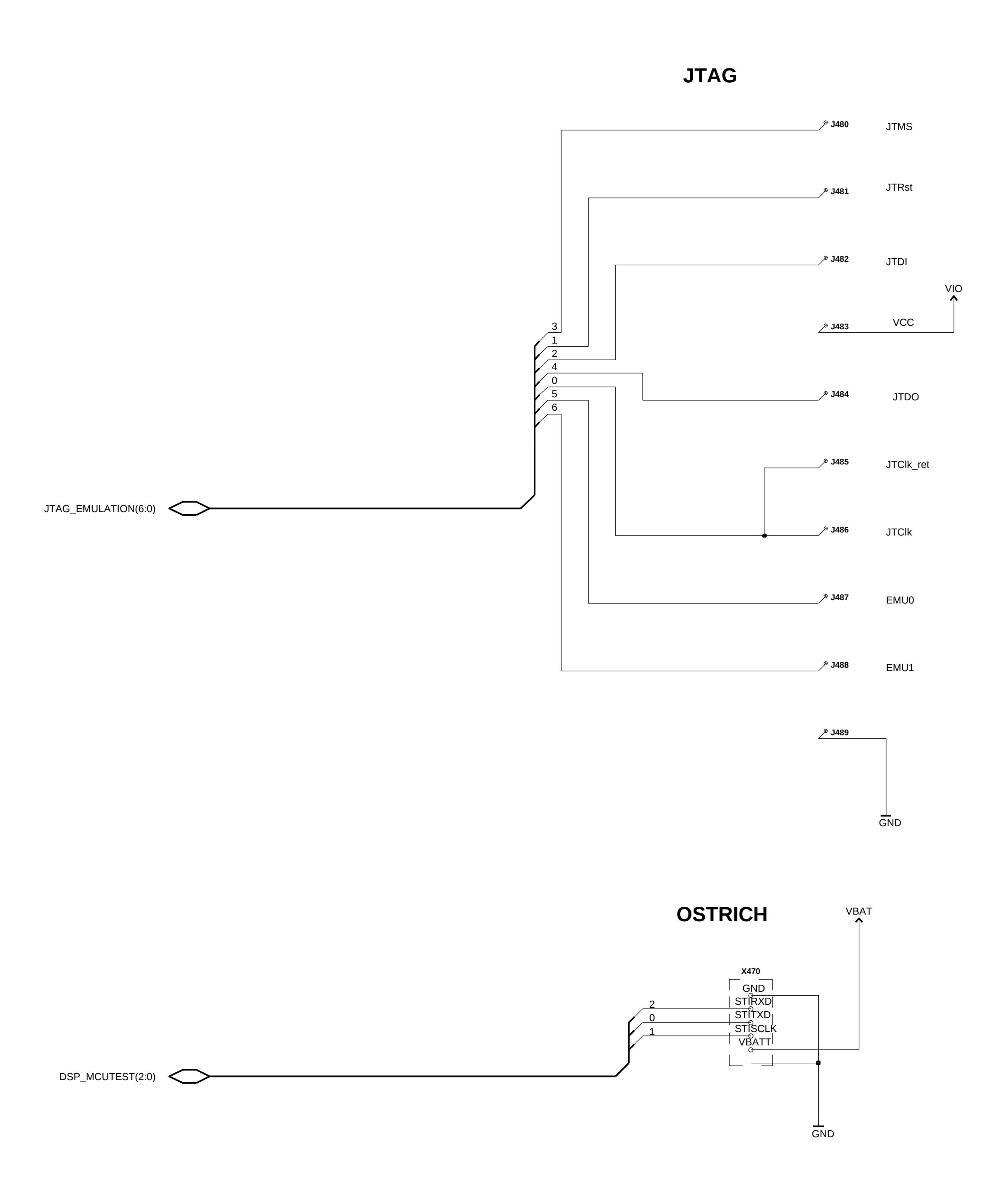
SIM Reader



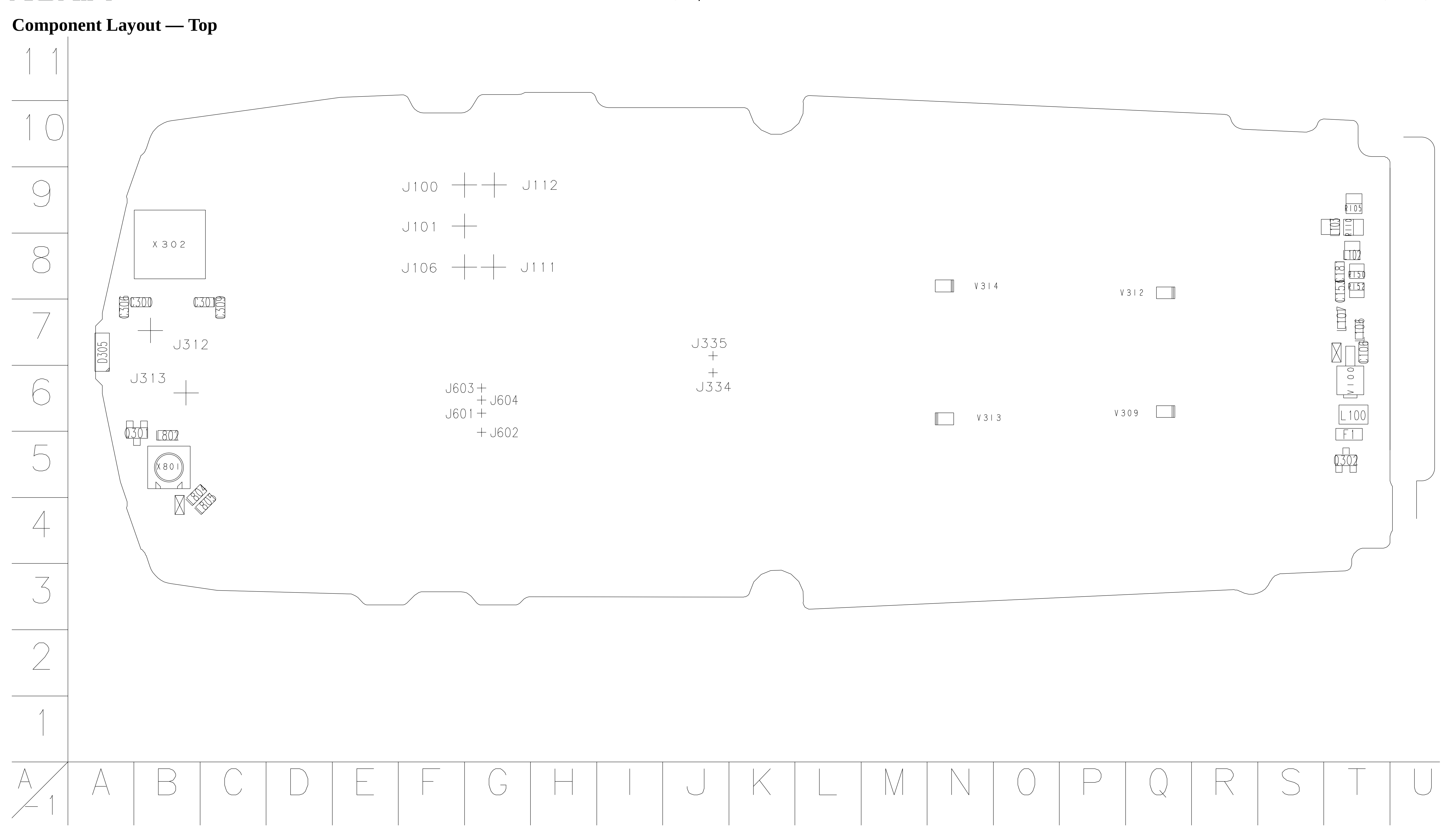
BB-RF Interface



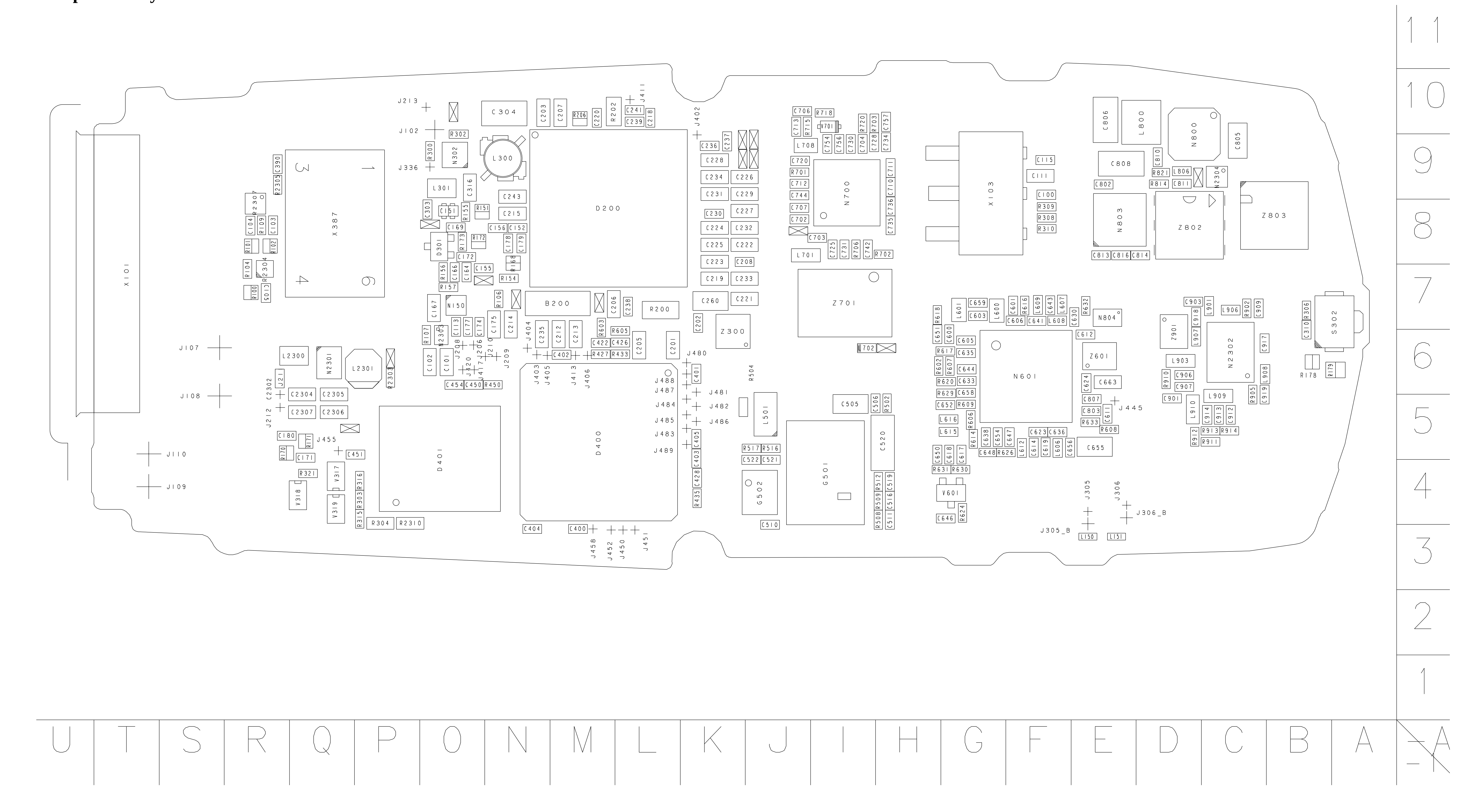
JTAG and Ostrich Connectors



Component Layout — Top



Component Layout — Bottom



Component Values — Top



Component Values — Bottom

